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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

I hereby certify that this transmittal of the below described document is being deposited with the United States Postal Service in an envelope bearing First Class Postage and addressed to the U.S. Patent and Trademark Office, Washington, D.C., 20231, on the below date of deposit.			
Date of Deposit:	11/14/02	Name of Person Making the Deposit:	KATHERINE RINALDI
		Signature of the Person Making the Deposit:	<i>Katherine Rinaldi</i>

In re Application of: Warren Snyder

Serial No.: 09/ 975,104

Examiner: not yet assigned

Filed: 10/10/01

Art Unit: 2184

For: CAPTURING TEST/EMULATION AND ENABLING REAL-TIME DEBUGGING USING AN FPGA FOR IN-CIRCUIT EMULATION

**Assistant Commissioner for Patents
Washington, D.C. 20231**

AMENDMENT TRANSMITTAL

**RECEIVED
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Technology Center 2100

1. Transmitted herewith is an amendment for this application

- ☒ Transmitted herewith is an amendment for the above identified patent application.
(9 sheets)
- ☒ Transmitted herewith are 9 sheets of Supporting Documents A-D.

2. Applicant is other than a small entity

Extension of Term

3. The proceedings herein are for a patent application and the provisions of 37 C.F.R. 1.136 apply.

(a) [] Applicant petitions for an extension of time under 37 C.F.R. 1.136
(fees: 37 C.F.R. 1.17(a)-(d) for the total number of months checked below:)

<u>Extension</u>	<u>Fee</u>
[] one month	\$110.00
[] two months	\$400.00
[] three months	\$920.00
[] four months	\$1,960.00

Fee \$

If an additional extension of time is required, please consider this a petition therefor.

(b) [X] Applicant believes that no extension of term is required. However, this conditional petition is being made to provide for the possibility that applicant has inadvertently overlooked the need for a petition for extension of time.

Fee Calculation

4. The fee for claims (37 C.F.R. 1.16(b)-(d)) has been calculated as shown below:

(for other than a small entity)					
Fee Items	Claims Remaining After Amendment	Highest Number of Claims Previously Paid For	Present Extra Claims	Fee Rate	Total
Total Claims	17	- 20 =	0	x \$18.00	\$0.00
Independent Claims	3	- 3 =	0	x \$80.00	\$0.00
Multiple Dependent Claim Fee (one or more, first added by this amendment)				\$260.00	\$0.00
Total Fees					\$0.00

PAYMENT OF FEES

5. The full fee due in connection with this communication is provided as follows:
- ☒ [x] The Commissioner is hereby authorized to charge any additional fees associated with this communication or credit any overpayment to Deposit Account No.: 23-0085.
A duplicate copy of this authorization is enclosed.
- ☐ [] A check in the amount of \$
- ☐ [] Charge any fees required or credit any overpayments associated with this filing to Deposit Account No.: 23-0085.

Please direct all correspondence concerning the above-identified application to the following address:

WAGNER, MURABITO & HAO LLP
Two North Market Street, Third Floor
San Jose, California 95113
(408) 938-9060

Respectfully submitted,

Date: 11/14/2002

By: 

Anthony C. Murabito
Reg. No. 35,295



CYPR-CD00183

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IN THE UNITED STATES PATENT & TRADEMARK OFFICE

IN RE APPLICATION OF:

Warren SNYDER

SERIAL NO: 09/975,104

FILED: OCTOBER 10, 2001

FOR: CAPTURING TEST/EMULATION
AND ENABLING REAL-TIME
DEBUGGING USING AN FPGA
FOR IN-CIRCUIT EMULATION

:

: GROUP ART UNIT:

:

: EXAMINER:

#2 | Preliminary Amendment H
J. J. Snyder
11/20/01

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PRELIMINARY AMENDMENT

Technology Center 2100

COMMISSIONER FOR PATENTS
WASHINGTON, D.C. 20231

SIR:

Prior to examination of the above-identified application, consideration of the following
Amendments and Remarks is respectfully requested.

IN THE SPECIFICATION

Please replace the paragraph at page 1, lines 12-28 with the following paragraph:

This application is related to U.S. Patent Application Serial No. 09/975,115, docket number CYPR-CD00182, to Warren Snyder, et al., entitled "IN-SYSTEM CHIP EMULATOR ARCHITECTURE"; and to U.S. Patent Application Serial No. 09/975,030, docket number CYPR-CD00185, to Warren Snyder, et al., entitled "EMULATOR CHIP/BOARD ARCHITECTURE AND INTERFACE"; and to U.S. Patent Application Serial No. 09/975,338, docket number CYPR-CD00186, to Warren Snyder, et al., entitled "METHOD FOR BREAKING EXECUTION OF (TEST) CODE IN A DUT AND EMULATOR CHIP ESSENTIALLY SIMULTANEOUSLY AND HANDLING COMPLEX BREAKPOINT

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